BEE 271 Digital circuits and systems Spring 2017 Lecture 2: Logic circuits and Karnaugh maps

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Topics

- 1. Review
	- a. Binary numbers
	- b. Boolean algebra
- 2. minterms and Maxterms
- 3. Sum of products
- 4. Product of sums
- 5. Karnaugh maps

We only have bits

We represent everything in bits, where each bit can be only a 0 or a 1.

Implemented as voltage levels in a digital circuit, e.g., shown here for TTL.

Adding zeroes to the left doesn't change the value.

In decimal

001492 = 1492

In binary

001011 = 1011

When we add numbers we get carries.

In decimal *110* 1492 + 525 2017 In binary *011* 1011 + 011 1110

Binary numbers

Numbering of the individual bits is from least significant bit (LSB) to most significant bit (MSB).

If $b0 = 0$, the number is even. If $b0 = 1$, the number is odd.

Each bit represents a power of 2.

Value of a binary number

Hex

- 1. Hard to read long strings of nothing but $1's$ and $0's$.
- 2. So we break it up into groups of 4 bits called *nibbles*, starting *at the LSB*.
- 3. Take each 4-bit group as a value from 0 to 15.
- 4. Values 10 to 15 written as A to F.

0111010010011111

0111 0100 1001 1111 7 4 9 F

In hex A12D $16^0 = 1$ $16^1 = 16$ $-16^2 = 256$ 16^3 = 4096

What is binary 10100101 in decimal and hex?

What is binary 10100101 in decimal and hex?

Value =
$$
1*1 + 0*2 + 1*4 + 0*8 + 0*16
$$

+ $1*32 + 0*64 + 1*128$
= $1 + 4 + 32 + 128$
= 165

 $10100101 = 10100101 = A5$ hex $= A5 = 10*16 + 5 = 165$

What is binary 1111100 in decimal and hex?

What is binary 1111100 in decimal and hex?

Value =
$$
0*1 + 0*2 + 1*4 + 1*8 + 1*16 + 1*32 + 1*64
$$

= $4 + 8 + 16 + 32 + 64$
= 124

Only 7 bits given, extend with high-order zeros. $10100101 = 1111100 = 01111100 = 7C$ hex $= 7*16 + 12 = 124$

Converting to binary

- 1. Repeatedly integer divide by 2 until the result is 0.
- 2. At each step, the remainder is the next bit, starting with the LSB.

Convert 12 to binary

(We start at the LSB because the lowest bit is just odd or even.)

12 base $10 = 1100$ binary = Hex C

Exercise: Convert 957 to binary

Exercise: Convert 957 to binary

957 decimal = 11 1011 1101 binary = 3BD hex

Exercise: Convert 1492 to hex

Exercise: Convert 1492 to hex

1492 decimal = 101 1101 0100 binary = 5D4 hex

Chapter 2

Introduction to Logic Circuits

Boolean Algebra

- Values 0, 1 Variables A, B, C, Sum, DoorOpen, .. Operations NOT, AND, OR
- *Operation Written as*
- NOT X as \overline{X} , X' or X^*
- X AND Y X Y, X Y or X & Y

 $X O R Y$ $X + Y$

The basic gates.

AND If all inputs are true, the output is true.

OR If any input is true, the output is true.

NOT The output is the inverse of the input.

A more complete set of gates

Truth tables

We describe Boolean functions with truth tables.

Addition of one-bit binary numbers.

Truth tables

Deriving Boolean equations from truth tables:

OR together *product* terms for each truth table row where the function is 1.

If input variable is 0, it appears in complemented form; if 1, it appears uncomplemented.

Truth tables

Deriving Boolean equations from truth tables:

Example: a full adder

Sum =

Cout =

Example: a full adder

Sum = $A' B' C$ in + $A' B C$ in' + $A B' C$ in' + $A B C$ in Cout = A' B Cin + A B' Cin + A B Cin' + A B Cin

1. Output should = 1 if the majority of the inputs = 1.

4. Enumerate all the possible input combinations.

5. Fill in the outputs.

5. Write the equation summing up all the 1's.

A deeper dive into Boolean algebra

Boolean algebra

Named after George Boole, who published an algebraic description of the processes involved in logical thought and reasoning in 1849.

https://en.wikipedia.org/wiki/George_Boole
Boolean algebra

In the 1930s, used by Claude Shannon to describe circuits built with switches, and thus with logic circuits.

https://en.wikipedia.org/wiki/Claude Shannon

ax·i·om

/ˈaksēәm/

noun

a statement or proposition that is regarded as being established, accepted, or self-evidently true.

"the **axiom that** supply equals demand"

synonyms: accepted truth, general truth, dictum, truism, principle; More

MATHEMATICS

a statement or proposition on which an abstractly defined structure is based.

Axioms of Boolean Algebra

- 1a. $0 \cdot 0 = 0$
- 1b. $1 + 1 = 1$
- 2a. $1 \cdot 1 = 1$
- 2b. $0 + 0 = 0$
- 3a. $0 \cdot 1 = 1 \cdot 0 = 0$
- $3b. 1 + 0 = 0 + 1 = 1$
- 4a. If $x = 0$, then $x' = 1$
- 4b. If $x = 1$, then $x' = 0$

Notice the *duality*:

Single-variable theorems

- 5a. $x \cdot 0 = 0$ 5b. $x + 1 = 1$
- 6a. $x \cdot 1 = x$
- 6b. $x + 0 = x$
- 7a. $x \bullet x = x$ Replication
- 7b. $x + x = x$
- 8a. $x \bullet x' = 0$
- 8b. $x + x' = 1$
- 9. $(x')' = x$

Easily proved by *perfect induction,* trying all the possibilities.

2 and 3-variable properties

Easily proved by *perfect induction,* trying all the possibilities.

2 and 3-variable properties

Easily proved by *perfect induction,* trying all the possibilities.

Can prove Boolean theorems by

- 1. Perfect induction
- 2. Algebraically
- 3. Venn diagrams

DeMorgan's theorem by perfect induction $(x • y)' = x' + y'$

Proof of DeMorgan's theorem by perfect induction, enumerating all the possibilities in a truth table.

Algebraic proof of the Combining theorem

Combining theorem: 14a. $x \bullet y + x \bullet y' = x$ 14b. $(x + y) \bullet (x + y') = x$

$$
x \bullet y + x \bullet y' = x (y + y')= x
$$

$$
(x+y)(x+y') = x x + x y' + x y + y y'
$$

= x + x (y' + y) + 0
= x + x = x

Algebraic proof of the Consensus theorem

Prove: $x y + x' z + y z = x y + x' z$ $(x + x') = 1$ $y z = (x + x') y z = x y z + x' y z$ Substituting back into the original LHS: $x y + x' z + y z = x y + x' z + (x y z + x' y z)$ $= x y + x y z + x' z + x' y z$ $= x y (1 + z) + x' z (1 + y)$ $=$ x y + x' z *Prove we can ignore this term.*

Proof of DeMorgan's Theorem

 $X' + Y'$

Bubble pushing

DeMorgan's theorem in terms of logic gates.

Operator precedence

Example:
$$
x + y \cdot z' = x + (y \cdot (z'))
$$

Parentheses can be used to specify a different order of evaluation, for example:

$$
((x+y) \bullet z)'
$$

We tend to omit the • when the meaning is clear.

Minimization

Often relies on these Boolean theorems:

- 1. $a + a b = a (1 + b) = a$
- 2. $a b + a b' = a (b + b') = a$
- 3. $(a + b)(a + b') = a$
- $4. a + a = a$

Synthesis is the process of beginning with a description of the *desired* functional behavior and then generating a circuit that *realizes* that behavior.

$$
f(x1, x2) = x1'x2' + x1'x2 + x1x2
$$

Exercise: Synthesize this function

To do that, we add a copy of the middle term. We can do that because $x + x = x$. Exercise: Synthesize this function

$$
f(x1, x2) = x1'x2' + x1'x2 + x1x2
$$

Since $x + x = x$, we can replicate the middle term: f(x1, x2) = $x1'$ x2' + $x1'$ x2 + $x1'$ x2 + $x1$ x2 Using the distributive property: f(x1, x2) = x1' (x2' + x2) + (x1' + x1) x2 $= x1' + x2$

(b) Minimal-cost realization

Figure 2.20. Two implementations of the function in Figure 2.19.

(a) Conveyor and sensors

Reject if the ball is too large or both too small and too light.

(b) Truth table

Figure 2.21. A bubble gumball factory.

We could OR together one term per row where $f = 1$.

 $f = s1' s2' s3 + s1' s2 s3 + s1 s2' s3 + s1 s2 s3' + s1 s2 s3$ Duplicating last term and collecting terms: $= s1' s3 (s2' + s2) + s1 s3 (s2' + s2) + s1 s2 (s3' + s3)$ $= s1' s3 + s1 s3 + s1 s2$ $= (s1' + s1) s3 + s1 s2$ $= s3 + s1 s2$

Two ways to synthesize a function

Sum of products: Include all rows where $f = 1$ using minterms.

Product of sums: Exclude all rows where $f = 0$ using Maxterms.

minterms and Maxterms

A *minterm* is 1 for only one row.

A *Maxterm* is 0 for only one row.

Minterms are small m

Maxterms are big M

Minterms (small m)

A *minterm* is 1 for only one row.

Maxterms (big M)

 $(x1, x2, x3) = (1, 0, 1).$

A *maxterm* is a 0 for only one

Sum of products

Include all rows where $f = 1$ using minterms.

$$
f = \sum \left(m_i \bullet f_i \right)
$$

Where $f_{\boldsymbol{i}}$ is the desired result for row $i.$ If f_i is 0, we can eliminate that term.

Using minterms for the rows where we want ones:

Using minterms for the rows where we want ones:

$$
f = \sum m(1, 4, 5, 6) = m1 + m4 + m5 + m6
$$

= (m1 + m5) + (m4 + m6)
= (x1' x2' x3 + x1 x2' x3) + (x1 x2' x3' + x1 x2 x3')
= (x1' + x1) x2' x3 + x1 (x2' + x2) x3'
= x2' x3 + x1 x3'

Product of sums

Exclude all rows where $f = 0$ using Maxterms.

$$
f = \prod (M_i + f_i)
$$

Where $f_{\boldsymbol{i}}$ is the desired result for row $i.$ If f_i is 1, we can eliminate that term.

Using Maxterms for the rows where we want zeros:

 $f =$

Using Maxterms for the rows where we want zeros:

 $f = \Pi M(0, 2, 3, 7) = M0 \cdot M2 \cdot M3 \cdot M7$

Using Maxterms for the rows where we want zeros:

$$
f = \Pi M(0, 2, 3, 7) = M0 \cdot M2 \cdot M3 \cdot M7
$$

= $(x1 + x2 + x3) (x1 + x2' + x3) (x1 + x2' + x3') (x1' + x2' + x3')$

Using Maxterms for the rows where we want zeros:

$$
f = \Pi M(0, 2, 3, 7) = M0 \cdot M2 \cdot M3 \cdot M7
$$

= $(x1 + x2 + x3) (x1 + x2' + x3) (x1 + x2' + x3') (x1' + x2' + x3')$
= $((x1 + x3) + x2) ((x1 + x3) + x2') (x1 + (x2' + x3')) (x1' + (x2' + x3'))$

Using Maxterms for the rows where we want zeros:

$$
f = MO \cdot M2 \cdot M3 \cdot M7
$$

= $(x1 + x2 + x3)(x1 + x2' + x3)(x1 + x2' + x3')(x1' + x2' + x3')$
= $((x1 + x3) + x2) ((x1 + x3) + x2') (x1 + (x2' + x3'))(x1' + (x2' + x3'))$

Combining theorem:

14a.
$$
x \cdot y + x \cdot y' = x
$$

14b. $(x + y) \cdot (x + y') = x$

 $f = ((x1 + x3) + x2) ((x1 + x3) + x2') (x1 + (x2' + x3')) (x1' + (x2' + x3'))$ $= (x1 + x3) (x2' + x3')$

Using Maxterms for the rows where we want zeros:

$$
f = \Pi M(0, 2, 3, 7) = M0 \cdot M2 \cdot M3 \cdot M7
$$

= $(x1 + x2 + x3) (x1 + x2' + x3) (x1 + x2' + x3') (x1' + x2' + x3')$
= $((x1 + x3) + x2) ((x1 + x3) + x2') (x1 + (x2' + x3')) (x1' + (x2' + x3'))$
= $(x1 + x3) (x2 + x2') (x2' + x3') (x1 + x1')$
= $(x1 + x3) (x2' + x3')$

(b) A minimal product-of-sums realization

Figure 2.24. Two realizations of the function.

Exercise: A 3-variable function we'd like to synthesize

Are POS and SOP solutions always equivalent cost?

- 1. Does it matter how many rows are 1s and how many are 0s? Why or why not?
- 2. Does it matter which rows are 1s or 0s in relation to each other?

Karnaugh maps

Want simplest forms but the algebra is difficult.

Karnaugh maps

Invented by Maurice Karnaugh in 1954 as a graphical method for simplifying Boolean equations.

[http://www.ithistory.org/sites/default/files/honor](http://www.ithistory.org/sites/default/files/honor-roll/Maurice%20Karnaugh.jpg)roll/Maurice%20Karnaugh.jpg

Karnaugh maps

Map rows in a truth table to cells in a matrix. May choose either assignment of columns and rows.

Fill in the desired output values.

Use the Combining property to group neighboring cells where the output should be the same.

14a.
$$
x \cdot y + x \cdot y' = x
$$

Form the minimal SOP solution.

A function of 3 variables

A function of four variables

SOP terminology

Each row or cell where f = 1 is an *implicant*. The *prime implicants* are a' and b.

Cover A collection of implicants that account for all cases for which the output $= 1$.

Essential prime implicant

A *prime implicant* that must be included in any *cover*.

a' and b form a *cover* for f. Both are *essential prime implicants*.

For a function of *n* variables, there will be 2*ⁿ* rows in the truth table and 2*ⁿ* cells in the Karnaugh map.

The number of cells in an implicant must be a power of 2.

For a function of *n* variables, if an implicant has *k* literals, it must cover 2*n*-*^k* cells.